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**FACSIMILE TRANSMISSION**

**DATE:** April 6, 2005

**TO:** Office of Initial Patent Examination's Filing  
Receipt Corrections  
@ U.S. Patent and Trademark Office Fax No.: 703-746-9195

**FROM:** Richard D. Ratchford Jr.

**NO. OF PAGES TO FOLLOW:** 3

**MESSAGE:** Re: U.S. Patent Appln. Serial No. 10/790,262  
**SEMICONDUCTOR MEMORY DEVICE CAPABLE OF  
GENERATING VARIABLE CLOCK SIGNALS ACCORDING  
TO MODES OF OPERATION**  
Group Art Unit: 2827  
Our Docket No. 8836-232 (ID12245-US)

Dear Sir/Madam:

Attached hereto, please find a Request for a Corrected Filing Receipt in connection with the above-identified patent application.

Upon review and acceptance, please provide us with an updated Corrected Filing Receipt. Thank you.

Richard D. Ratchford Jr.

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PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Nam-Seog KIM

Docket No.: 8836-232  
(ID12245-US)

Serial No.: 10/790,262

Group Art Unit: 2827

Filed: March 1, 2004

For: SEMICONDUCTOR MEMORY DEVICE CAPABLE OF  
GENERATING VARIABLE CLOCK SIGNALS ACCORDING  
TO MODES OF OPERATIONCommissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450REQUEST FOR CORRECTED FILING RECEIPT

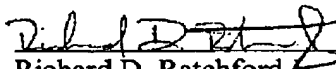
Sir:

Please issue a corrected Filing Receipt for the above-identified application  
with the following correction:

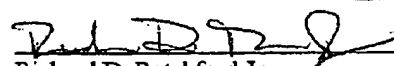
please correct the lead inventor's name by deleting "Nam-Soeg Kim" and  
inserting -- Nam-Seog Kim --.

The original Filing Receipt is attached herewith for correction with the  
change indicated in red.

Respectfully submitted,

F. CHAU & ASSOCIATES, LLC  
130 Woodbury Road  
Woodbury, NY 11797  
(516) 692-8888  
Richard D. Ratchford Jr.  
Reg. No. 53,865  
Attorney for ApplicantCERTIFICATE OF FACSIMILE

I hereby certify that this correspondence is being facsimiled to the Office of Initial Patent  
Examination's Filing Receipt Corrections at the U.S. Patent and Trademark Office on April 6, 2005.

Dated: April 6, 2005  
Richard D. Ratchford Jr.



## UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/790,262	03/01/2004	2818	770	8836-232 (ID12245-US)	14	19	3

CONFIRMATION NO. 4733

Frank Chau  
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East Meadow, NY 11554

## FILING RECEIPT



\*OC000000012720393\*

Date Mailed: 05/20/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) *Nam-Seog Kim*

*Nam-Seog Kim*, Seoul, KOREA, REPUBLIC OF;  
Uk-Rae Cho, Suwon-si, KOREA, REPUBLIC OF;  
Yong-Jin Yoon, Seoul, KOREA, REPUBLIC OF;

## Assignment For Published Patent Application

Samsung Electronics Co., Ltd.;

## Domestic Priority data as claimed by applicant

## Foreign Applications

REPUBLIC OF KOREA 2003-15761 03/13/2003

If Required, Foreign Filing License Granted: 05/19/2004

Projected Publication Date: 09/16/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor memory device capable of generating variable clock signals according to modes of operation

**Preliminary Class**

365

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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